

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Randy Bonella et al. Art Unit : 2115
Serial No. : 09/662,054 Examiner : Dennis M. Butler
Filed : September 15, 2000 Notice of Allowance Date:
Assignee : Intel Corporation March 7, 2005
Title : DIGITAL SYSTEM OF ADJUSTING DELAYS ON CIRCUIT BOARDS

Mail Stop Issue Fee

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

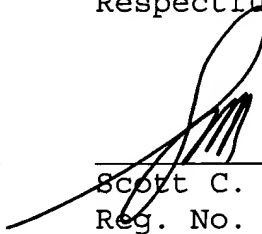
RESPONSE TO NOTICE OF ALLOWANCE

In response to the Notice of Allowance mailed March 7, 2005, enclosed are a completed issue fee transmittal form PTOL-85b and a check for \$1,400 for the required fee.

Please apply any charges or credits to our Deposit Account No. 06-1050.

Respectfully submitted,

Date: April 19, 2005



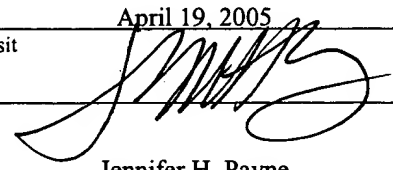
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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

April 19, 2005
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